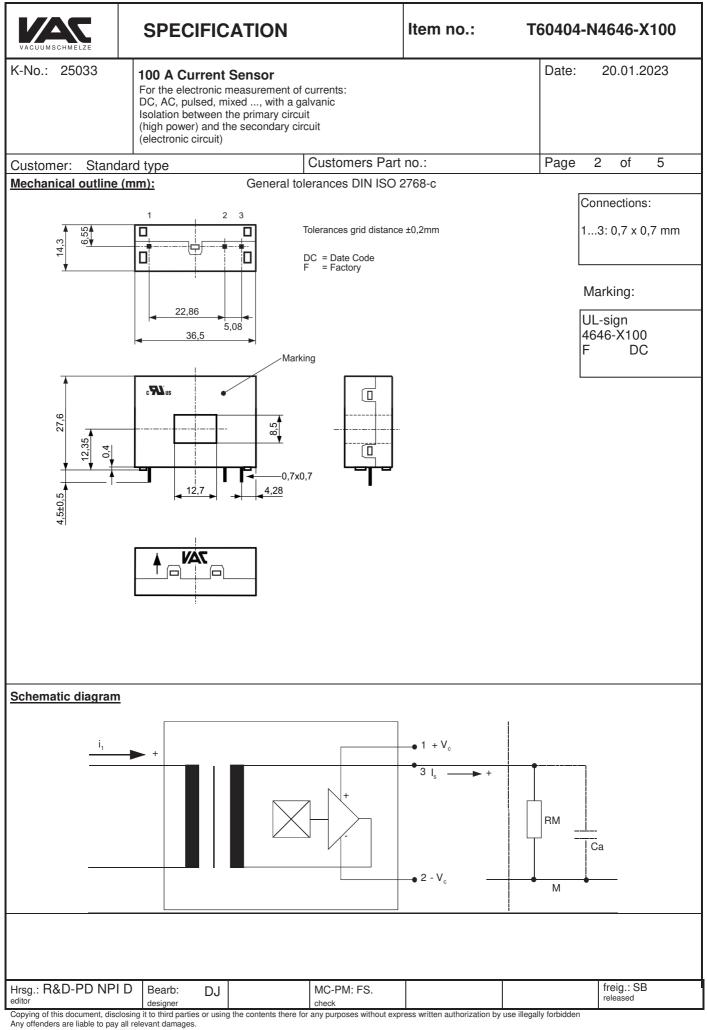
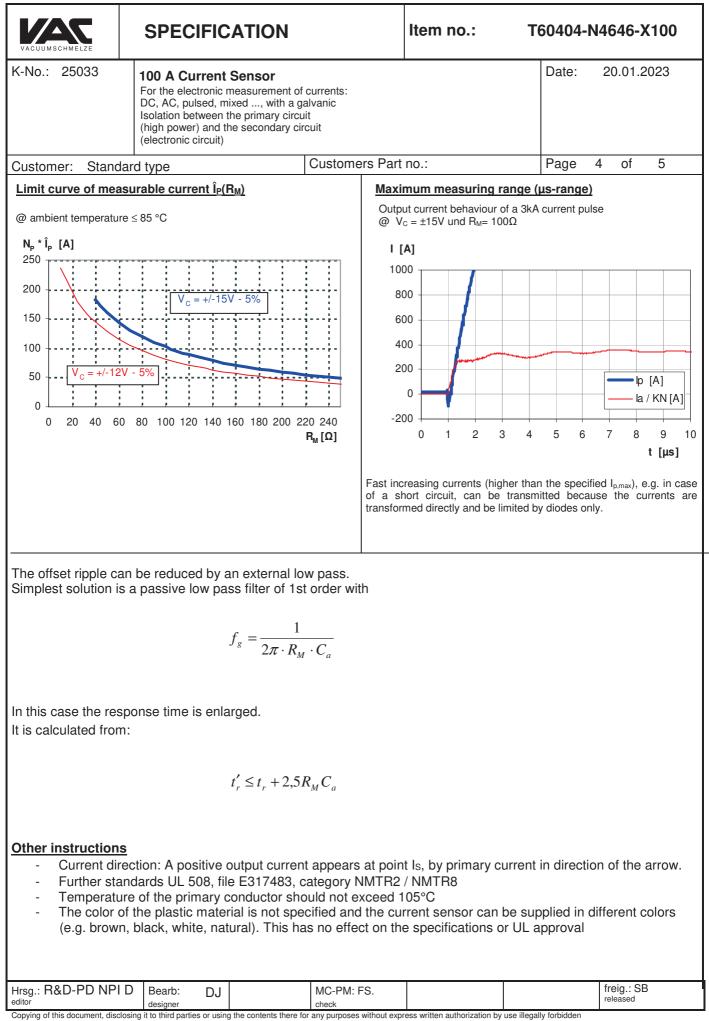
VACUUMSCHMELZE	SPECIFICATION Item no.:		no.: T	T60404-N4646-X100	
K-No.: 25033	No.: 25033 100 A Current Sensor For the electronic measurement of currents: DC, AC, pulsed, mixed, with a galvanic Isolation between the primary circuit (high power) and the secondary circuit (electronic circuit)				
Customer: Stan	dard type Cus	tomers Part no.:		Page 1	of 5
 Description Closed loop (comp Current Sensor wit field probe Printed circuit boar Casing and material 	ensation) h magnetic d mounting Characteristics • Excellent accuracy • Very low offset current • Very low temperature de current drift	pendency and offset fset current	Applications Mainly used for stat applications: • AC variabel sp drives • Static converte • Battery supplie • Switched Mod • Power Supplie • Uninterruptabl	ionary operati beed drives ar ers for for DC ed applications le Power Supp es for welding	nd servo motor motor drives s blies (SMPS) applications
Electrical data – R	atings				
I _{PN}	Primary nominal r.m.s. current			100	А
Rм	Measuring resistance $V_{C}=\pm 12V$			10 200	Ω
	V _C =± 15V			40400	Ω
Isn Kn	Secondary nominal r.m.s. current Turns ratio			100 1:1000	mA
<u> Accuracy – Dynan</u>	nic performance data	min.	typ.	max.	Unit
IP,max	Max. measuring range @ V _C = ±12V, R _M = 10 Ω (t _{max} = 10se @ V _C = ±15V, R _M = 40 Ω (t _{max} = 10se				A A
Х	Accuracy @ I _{PN} , T _A = 25°C		0.1	0.5	%
εL	Linearity			0.1	%
lo	Offset current @ IP=0, TA= 25°C		0.04	0.1	mA
tr	Response time		1		μs
∆t (I _{P,max}) f	Delay time at di/dt = 100 A/µs Frequency bandwidth	DC20	200		ns kHz
General data		min.	typ.	max.	Unit
TA	Ambient operating temperature	-40	cyp.	+85	°C
Ts	Ambient storage temperature	-40		+90	°C
m	Mass		14		g
Vc	Supply voltage		±12 or ±15	±15.75	V
lc	Current consumption Constructed and manufactored and te Reinforced insulation, Insulation mate			l (primary vs	mA . secondary)
Sclear	Clearance (component without solder pa		aog.00 2		mm
Screep	Creepage (component without solder pa	,			mm
V _{sys} V _{work}	System voltage overvoltage categor Working voltage (table 7 acc. to EN6	5		600	V
	over voltage catego	ry 2 RMS		1000	V
UPD	Rated discharge voltage	peak va	alue	1225	V
•	lifference acc to UL 508	RMS .		600	V
	us and peak currents at defined tempe		,		
Supply voltage ±12		Supply voltage ±15			
T _A 85 °C	85 °C 70 °C 55 °C	T _A 85 °C	85 °C 70 °C	55 °C	
IP 60 A IP,max 235 A	100 A 80 A 100 A 149 A 241 A 246 A	IP 50 A I _{P,max} 182 A	75 A 70 A 130 A 184 A	100 A 186 A	
I _{P,max} 235 A R _M 10 Ω	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	I _{P,max} 182 Α R _M 40 Ω	130 A 184 A 70 Ω 40 Ω	40 Ω	
Date Name I	suue Amendment				
20.01.2023 DJ	81 Other instructions on sheet 4 changed (size 4,28 added). Minor change	. The color of the plastic	material added.	Mechanical ou	Itline changed
Hrsg.: R&D-PD NF		-PM: FS.			freig.: SB released
editor					



Electrical Data (invo V _{Ctot} Rs X _{Ti} loges lot loT	100 A Current Sensor For the electronic measurement of currents: DC, AC, pulsed, mixed, with a galvanic Isolation between the primary circuit (high power) and the secondary circuit (electronic circuit) ard type Customers F estigate by a type checking) Maximum supply voltage (without function) ±15.75 to ±18 V: for 1s per hour Secondary coil resistance @ T _A =85°C Temperature drift of X @ T _A = -40 +85 °C	art no.: min.	typ.	Date:	20.01.2023 3 of 5
Electrical Data (inve V _{Ctot} Rs X _{Ti} I _{oges} I _{ot} I _{oT}	Add type estigate by a type checking) Maximum supply voltage (without function) ±15.75 to ±18 V: for 1s per hour Secondary coil resistance @ T _A =85°C		tvo	Page	3 of 5
V _{Ctot} Rs X _{Ti} Ioges Iot IoT	Maximum supply voltage (without function) ±15.75 to ±18 V: for 1s per hour Secondary coil resistance @ T _A =85°C	min.	typ		
V _{Ctot} Rs X _{Ti} Ioges Iot IoT	Maximum supply voltage (without function) ±15.75 to ±18 V: for 1s per hour Secondary coil resistance @ T _A =85°C	min.	typ		
Rs XTi Ioges Iot IoT	± 15.75 to ± 18 V: for 1s per hour Secondary coil resistance @ T _A =85°C			max.	Unit
XTi Ioges Iot IoT	, _			±18	V
loges lot loT	Temperature drift of X @ T _A = -40 +85 °C			38.5	Ω
lot loт				0.1	%
loт	Offset current (including Io, Iot, Iot)			0.14	mA
	Long term drift Offset current Io		0.05		mA
lau.	Offset current temperature drift $I_0 @ T_A = -40$		0.05		mA
Іон	Hyteresis current @ IP=0 (caused by primary curr	ent 10 x I _{PN})	0.05	0.1	mA
$\Delta I_0 / \Delta V_C$	Supply voltage rejection ratio			0.01	mA/V
loss	Offset ripple (with1 MHz- filter first order)			0.2	mA
l _{oss}	Offset ripple (with 100 kHz- filter first order)		0.04	0.075	mA
loss Ck	Offset ripple (with 20 kHz- filter first order) Maximum possible coupling capacity (primary -		0.015 6	0.025	mA pF
spection (Measure	ment after temperature balance of the samples at room	temperature)			
nspection (Measure K _N (N ₁ /N ₂) (V)	ment after temperature balance of the samples at room M3011/6 Transformation ratio (IP=100A, 40-	. ,		1 : 1000 ± 0,	5 %
```		. ,		1 : 1000 ± 0, < 0.1	5 % mA
K _N (N ₁ /N ₂ ) (V)	M3011/6Transformation ratio (IP=100A, 40-M3226Offset currentM3014:Test voltage, rms, 1 s	. ,	_		
$\begin{array}{c} K_{N}(N_{1}/N_{2}) & (V) \\ I_{0} & (V) \\ V_{d} & (V) \end{array}$	M3011/6Transformation ratio (IP=100A, 40-M3226Offset current	30 Hz)		< 0.1	mA
$\begin{array}{c} K_{N}(N_{1}/N_{2}) & (V) \\ I_{0} & (V) \\ V_{d} & (V) \\ V_{e} & (AC) \end{array}$	M3011/6Transformation ratio (IP=100A, 40-M3226Offset currentM3014:Test voltage, rms, 1 spin 1 – 3 vs. holeQL 1/S4)Partial discharge voltage acc.M303with Vvor (RMS)	30 Hz)		< 0.1 1.8 1300	mA kV V
K _N (N1/N2)         (V)           Io         (V)           Vd         (V)           Ve         (Additional of the second of the sec	M3011/6       Transformation ratio (IP=100A, 40- M3226         Offset current         M3014:       Test voltage, rms, 1 s pin 1 – 3 vs. hole         QL 1/S4)       Partial discharge voltage acc.M303 with Vvor (RMS)         3 to hole)	30 Hz) 4 (RMS)	prm)	< 0.1 1.8 1300	mA kV V
K _N (N1/N2)         (V)           Io         (V)           Vd         (V)           Ve         (Addressed)	M3011/6       Transformation ratio (IP=100A, 40- M3226         M3014:       Test voltage, rms, 1 s pin 1 – 3 vs. hole         QL 1/S4)       Partial discharge voltage acc.M30 with Vvor (RMS)         3 to hole)       HV transient test according to M3064 (1,2 µs /	30 Hz) 4 (RMS)	,	< 0.1 1.8 1300 1625 8	mA kV V V
K _N (N ₁ /N ₂ )         (V)           Io         (V)           Vd         (V)           Ve         (Additional of the second of the	M3011/6       Transformation ratio (IP=100A, 40- M3226         Offset current         M3014:       Test voltage, rms, 1 s pin 1 – 3 vs. hole         QL 1/S4)       Partial discharge voltage acc.M303 with Vvor (RMS)         3 to hole)	30 Hz) 4 (RMS)	orm) (5 s)	< 0.1 1.8 1300 1625	mA kV V V

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VACUUMSC	HMELZE	SPECIFICAT	ION	Item no.:	T60404-N4646-X100
K-No.: 2	25033	<b>100 A Current Sen</b> For the electronic mease DC, AC, pulsed, mixed . Isolation between the pr (high power) and the sec (electronic circuit)	urement of currents: , with a galvanic imary circuit		Date: 20.01.2023
Custome	r: Standa	urd type	Customers	Part no.:	Page 5 of 5
<u>Explanati</u>	on of sever	al of the terms used in	n the tablets (in alphat	petical order)	
Іон:	Zero varia	ation after overloading w	vith a DC of tenfold the	rated value ( $R_M = R_{MN}$ )	
I _{Ot} :	Long term	n drift of I₀ after 100 tem	perature cycles in the ra	ange -40 bis 85 °C.	
tr:	Response	e time, measured as del	ay time at $I_P = 0.8 \cdot I_{Pma}$	ax between a rectangula	r current and the output current.
$\Delta t$ (I _{Pmax} ):	Delay tim	e between IPmax and the	output current ia with a	primary current rise of c	li ₁ /dt = 100 A/μs.
Upd	Rated disch UPD	harge voltage (recurring = $\sqrt{2} * V_e / 1,5$	peak voltage separated	d by the insulation) prove	ed with a sinusoidal voltage $V_{\mbox{\scriptsize e}}$
Vvor	Defined vol test in IEC V _{vor}		of a sinusoidal voltage w	vith peak value of 1,875	* UPD required for partial discharge
V _{sys}	System vol	tage RMS value of r	rated voltage according	to IEC 61800-5-1	
Vwork	Working vo	Itage voltage accordi	ng to IEC 61800-5-1 wh	nich occurs by design in	a circuit or across insulation
X _{ges} (I _{PN} ):		of all possible errors ove $0 \cdot \left  \frac{I_{s}(I_{PN})}{K_{N} \cdot I_{SN}} - 1 \right $	er the temperature rang	e by measuring a currer	nt I _{PN} :
X:	Permissib	ble measurement error ir	n the final inspection at	RT, defined by	
	X =100	$\left  \frac{I_{SB}}{I_{SN}} - 1 \right $			
	where $I_{\text{SB}}$	is the output DC value	of an input DC current o	of the same magnitude a	is the (positive) rated current ( $I_0 = 0$ )
X _{Ti} :	Temperat obtained		ue orientated output ter	m. I _{SN} (cf. Notes on F _i ) ir	n a specified temperature range,
	$X_{\mathrm{Ti}} = 1$	$00 \cdot \left  \frac{I_{SB}(T_{A2}) - I_{SB}}{I_{SN}} \right $	Ι		
εL:	Linearity	fault defined by ${\cal E}_{ m I}$	$L = 100 \cdot \left  \frac{I_{P}}{I_{PN}} - \frac{I_{Sx}}{I_{SN}} \right $		
	Where IP			ut term. I _{SN} : see notes o	$f F_i (I_o = 0).$
Hrsg.: R&	D-PD NPI [	D Bearb: DJ	MC-PM: FS.		freig.: SB released