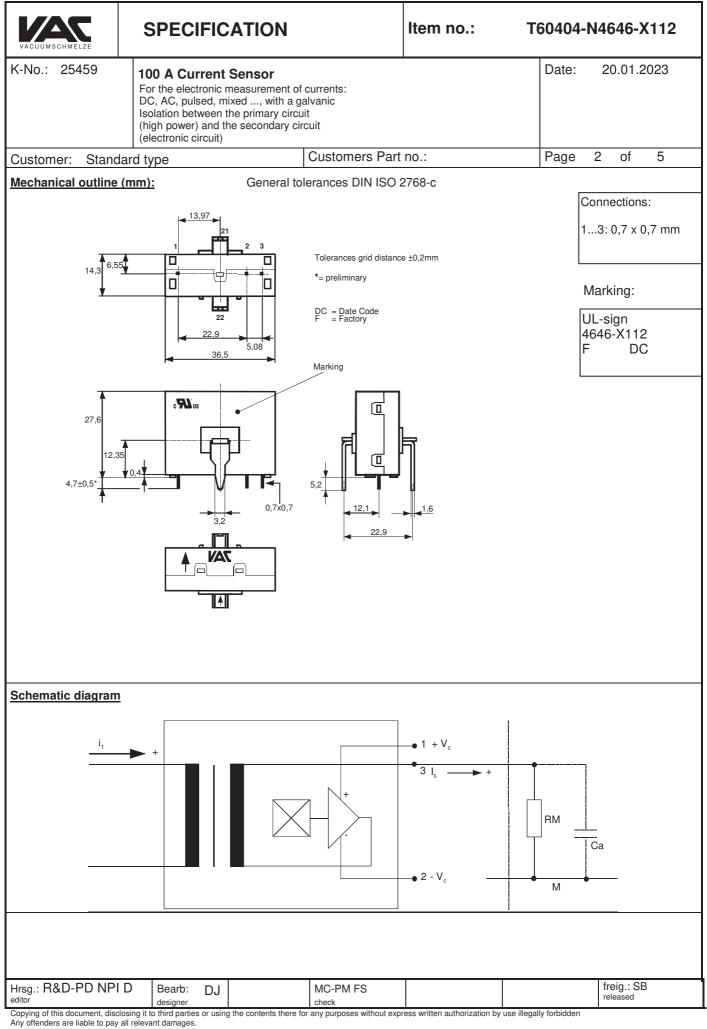
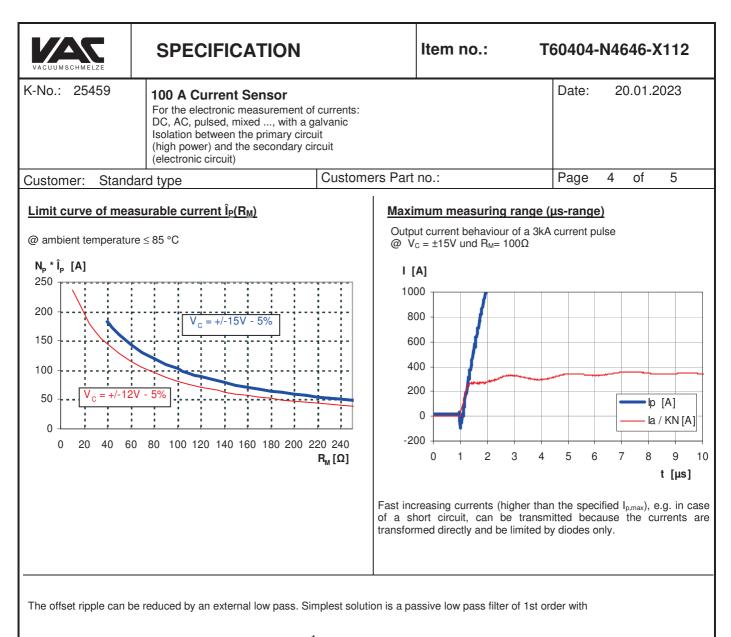
VACUUMSCHMELZE	SPECIFICATION	Item	no.:	Г60404-N4646-X112		
K-No.: 25459	100 A Current Sensor For the electronic measurement of curren DC, AC, pulsed, mixed, with a galvanin Isolation between the primary circuit (high power) and the secondary circuit (electronic circuit)			Date:	20.01.2023	
Customer: Stan	dard type Cus	tomers Part no.:		Page 1	of 5	
Description Closed loop (comp Current Sensor with field probe Printed circuit board Casing and materia	Characteristics ensation) magnetic Very low offset current Very low temperature de current drift	fset current	drives Static conver Battery supp Switched Mo Power Suppl	ationary operati speed drives an ters for for DC lied applications de Power Suppl ies for welding ole Power Suppli	nd servo motor motor drives s blies (SMPS) applications	
electrical data – R	atings					
Ipn Rm	Primary nominal r.m.s. current Measuring resistance V _C =± 12V V _C =± 15V			100 10 200 40400	Α Ω Ω	
Isn	Secondary nominal r.m.s. current			100	mA	
K _N	Turns ratio			1:1000		
Accuracy – Dynam	ic performance data	min.	typ.	max.	Unit	
I _{P,max}	Max. measuring range (a) $V_C = \pm 12V$, $R_M = 10 \Omega$ ($t_{max} = 10se$ (b) $V_C = \pm 15V$, $R_M = 40 \Omega$ ($t_{max} = 10se$				A A	
X	Accuracy @ IPN, TA= 25°C Linearity		0.1	0.5	%	
ε∟ Io	Offset current @ IP=0, TA= 25°C		0.04	0.1	mA	
tr	Response time		1	0.1	μs	
∆t (I _{P,max}) f	Delay time at di/dt = 100 A/µs Frequency bandwidth	DC2	200 00		ns kHz	
General data		min.	typ.	max.	Unit	
TA	Ambient operating temperature	-40		+85	°C	
Ts	Ambient storage temperature	-40		+90	°C	
m	Mass		14		g	
Vc Ic	Supply voltage Current consumption	±11.4	±12 or ±15 18	±15.75	V mA	
IC	Constructed and manufactored and te Reinforced insulation, Insulation mate		with EN 61800-5-	-1 (primary vs		
Sclear	Clearance (component without solder pa	,			mm	
Screep	Creepage (component without solder par			600	mm	
V _{sys} V _{work}	System voltage overvoltage categor Working voltage (table 7 acc. to EN6			600	V	
	over voltage catego	ry 2 RMS		1000	V	
Upd	Rated discharge voltage	peak v	value	1225	V	
Max. potential d	fference acc. to UL 508	RMS		600	V AC	
Maximal continuou Supply voltage ±12	us and peak currents at defined tempe	<u>ratures</u> Supply voltage ±15	5V:			
T _A 85 °C	85 °C 70 °C 55 °C	T _A 85 °C	85 °C 70 °C	55 °C		
I _P 60 A	100 A 80 A 100 A	IP 50 A	100 A 100 A			
I _{P,max} 235 A	149 A 241 A 246 A	I _{P,max} 182 A	129 A 161 A			
R _M 10 Ω	36 Ω 10 Ω 10 Ω	R _M 40 Ω	70 Ω <mark>50 Ω</mark>	40 Ω		
Date Name Is 20.01.2023 DJ	Suue Amendment 81 Other instructions on sheet 4 changed	The color of the place	matorial added	Minor obong		
17.04.13 KRe.	81 Other instructions on sheet 4 changed81 Mechanical outline: marking with UL-s					
Hrsg.: R&D-PD NP		-PM FS			freig.: SB	
		I			released	



VACUUMSCHMELZE		SPECIFICATION			Item n	Item no.:		T60404-N4646-X112		
No.: 254	459	For the ele DC, AC, p Isolation b	urrent Sensor ectronic measuren ulsed, mixed, w etween the prima er) and the second circuit)	nent of currents: <i>v</i> ith a galvanic ry circuit	I		Date:	20.01.2023		
ustomer:	Stand	ard type	,	Customer	s Part no.:		Page	3 of 5		
ootrical Dr	ata (inva	ostigato by a	type checking	<i>a)</i>						
		estigate by a		<u>47</u>	min.	typ.	max.	Unit		
V _{Ctot}			supply voltage (±18 V: for 1s pe			typ.	±18	V		
Rs			/ coil resistance				38.5	Ω		
RP		-	sistance @ T _{A=}			0,1		mΩ		
ζ _{Ti}			-	Γ _A = -40 +85 °C			0.1	%		
Oges		•	rent (including Ia				0.14	mA		
Ot			drift Offset curr			0.05		mA		
от				e drift I ₀ @ T _A = -40		0.05		mA		
0H			-	(caused by primary c	current 10 x I _{PN})	0.05	0.1	mA		
$\Delta I_0 / \Delta V_C$			tage rejection ra				0.01	mA/V		
OSS			le (with1 MHz- f				0.2	mA		
oss				z- filter first order)		0.04	0.075	mA		
i _{oss} C _k				filter first order) ng capacity (prima		0.015 6	0.025	mA pF		
		ement after ten	•	of the samples at rc	. ,					
K _N (N1/N2)	(V)	M3011/6	Transformatio	on ratio (I _P =100A, 4	. ,		1 : 1000 ± (
K _N (N1/N2)	(V) (V)	M3011/6 M3226	Transformation Offset current	on ratio (I _P =100A, 4 t	. ,		< 0.1	mA		
K _N (N ₁ /N ₂)	(V)	M3011/6	Transformatio	on ratio (I _P =100A, 4 t rms, 1 s	. ,					
Kn(N1/N2) Io Vd	(V) (V) (V)	M3011/6 M3226	Transformatic Offset curren Test voltage, pin 1 – 3 vs. ł	on ratio (I _P =100A, 4 t rms, 1 s nole urge voltage acc.M	40-80 Hz)		< 0.1	mA		
K _N (N ₁ /N ₂) I ₀ V _d V _e ype Testin ((V) (V) (V) (AC	M3011/6 M3226 M3014: QL 1/S4)	Transformatic Offset curren Test voltage, pin 1 – 3 vs. ł Partial discha with V _{vor} (RM	on ratio (I _P =100A, 4 t rms, 1 s nole urge voltage acc.M S)	40-80 Hz) 3024 (RMS)		< 0.1 1.8 1300	mA kV V		
K _N (N ₁ /N ₂) I ₀ V _d V _e vpe Testin Vw	(V) (V) (V) (AC	M3011/6 M3226 M3014: QL 1/S4) - 3 to hole) HV transie	Transformatic Offset curren Test voltage, pin 1 – 3 vs. ł Partial discha with V _{vor} (RM	on ratio (I _P =100A, 4 t rms, 1 s nole urge voltage acc.M	40-80 Hz) 3024 (RMS)	,	< 0.1 1.8 1300 1625 8	mA kV V V		
K _N (N ₁ /N ₂) I ₀ V _d V _e V <u>pe Testin</u> Vw V _d	(V) (V) (V) (AC	M3011/6 M3226 M3014: QL 1/S4) - 3 to hole) HV transie Testing vo	Transformatic Offset curren Test voltage, pin 1 – 3 vs. ł Partial discha with V _{vor} (RM	pn ratio (I _P =100A, 4 t rms, 1 s nole urge voltage acc.M S) g to M3064 (1,2 μ	40-80 Hz) 3024 (RMS)	rm) (5 s)	< 0.1 1.8 1300 1625 8 3,6	mA kV V V kV		
K _N (N ₁ /N ₂) I ₀ V _d V _e V <u>pe Testin</u> Vw V _d	(V) (V) (V) (AC	M3011/6 M3226 M3014: QL 1/S4) - 3 to hole) HV transie Testing vo	Transformatic Offset curren Test voltage, pin 1 – 3 vs. ł Partial discha with V _{vor} (RM	on ratio (I _P =100A, 4 t rms, 1 s nole urge voltage acc.M S)	40-80 Hz) 3024 (RMS)	,	< 0.1 1.8 1300 1625 8	mA kV V V		
K _N (N ₁ /N ₂) I ₀ V _d V _e <u>vpe Testin</u>	(V) (V) (V) (AC	M3011/6 M3226 M3014: QL 1/S4) - 3 to hole) HV transie Testing vo Partial disc	Transformatic Offset curren Test voltage, pin 1 – 3 vs. ł Partial discha with V _{vor} (RM	pn ratio (I _P =100A, 4 t rms, 1 s nole urge voltage acc.M S) g to M3064 (1,2 μ	40-80 Hz) 3024 (RMS)	,	< 0.1 1.8 1300 1625 8 3,6 1300	mA kV V V kV kV		
K _N (N ₁ /N ₂) I ₀ V _d V _e <u>vpe Testino</u> V _w V _d	(V) (V) (V) (AC	M3011/6 M3226 M3014: QL 1/S4) - 3 to hole) HV transie Testing vo Partial disc with V _{vor} (Transformatic Offset curren Test voltage, pin 1 – 3 vs. ł Partial discha with V _{vor} (RM	pn ratio (I _P =100A, 4 t rms, 1 s nole urge voltage acc.M S) g to M3064 (1,2 μ	40-80 Hz) 3024 (RMS) s / 50 μs-wave fo	,	< 0.1 1.8 1300 1625 8 3,6 1300	mA kV V V kV kV		



$$f_g = \frac{1}{2\pi \cdot R_M \cdot C_a}$$

In this case the response time is enlarged. It is calculated from:

$$t'_r \leq t_r + 2,5R_M C_a$$

Other instructions

- Current direction: A positive output current appears at point Is, by primary current in direction of the arrow.
- Further standards UL 508, file E317483, category NMTR2 / NMTR8
- Temperature of the primary conductor should not exceed 105°C
- The color of the plastic material is not specified and the current sensor can be supplied in different colors (e.g. brown, black, white, natural). This has no effect on the specifications or UL approval

Hrsg.: R&D-PD NPI D editor	Bearb: designer	DJ	MC-PM FS check			freig.: SB released
Copying of this document, disclosing it t Any offenders are liable to pay all releva			the contents there for any purposes without expre	ess written authorization by	use illegally forbidden	

VACUUMSC	HMELZE	SPE		ATION	I		Item no.:	те	60404-	N4646-	X112
K-No.:	25459 100 A Current Sensor For the electronic measurement of currents: DC, AC, pulsed, mixed, with a galvanic Isolation between the primary circuit (high power) and the secondary circuit (electronic circuit)						Date: 20.01.2023				
Custome	r: Stand	ard type			Custom	iers Part	no.:		Page	5 of	5
Explanat	on of seve	eral of the	<u>terms use</u>	d in the t	ablets (in al	phabetica	l order)				
Іон:	Zero var	iation after	overloadir	ng with a l	DC of tenfold	the rated	value (R _M = R _{MN}	1)			
lot:	Long ter	m drift of Io	after 100	temperatu	ure cycles in t	the range	40 bis 85 °C.				
tr:	Respons	se time, me	asured as	delay tim	e at I _P = 0,8	I _{Pmax} bet	ween a rectangi	ular currer	nt and th	e output c	urrent.
∆t (I _{Pmax}):	Delay tir	ne betweer	n I _{Pmax} and	the outpu	it current i _a w	ith a prima	ary current rise c	of di ₁ /dt =	100 A/µs		
U _{PD}	Rated disc UPD	charge volt = √2 * V	÷ .	ring peak	voltage sepa	rated by th	ne insulation) pro	oved with	a sinuso	idal voltaç	je V _e
Vvor		oltage is the 61800-5-1		ve of a sir	nusoidal volta	ge with pe	ak value of 1,87	75 * U _{PD} r	equired f	or partial	discharge
	V_{vor}	= 1,875	*U _{PD} / √2								
V _{sys}	System vo	oltage R	MS value	of rated v	voltage accor	ding to IE	C 61800-5-1				
Vwork	Working v	oltage v	oltage acc	ording to	IEC 61800-5-	1 which o	ccurs by design	in a circui	t or acro	ss insulati	on
X _{ges} (I _{PN}):		$00 \cdot \left \frac{I_{s}(I_{PN})}{K_{N} \cdot I_{s}} \right $	× 1	over the	temperature	range by r	neasuring a curi	rent I _{PN} :			
X:	Permiss	ible measu	rement err	or in the f	inal inspectio	n at RT, d	efined by				
	X =10	$0 \cdot \left \frac{I_{SB}}{I_{SN}} \right $	- 1								
	where Is	в is the out	tput DC va	lue of an i	input DC curr	ent of the	same magnitude	e as the (p	ositive)	rated curr	ent ($I_0 = 0$
X _{Ti} :	Tempera obtained		of the rated	value orie	entated outpu	ut term. Isn	(cf. Notes on Fi	i) in a spe	cified ten	nperature	range,
	$X_{\mathrm{Ti}} =$	$100 \cdot \left \frac{I_{SI}}{I_{SI}} \right $	$\frac{1}{I_{\text{SN}}} \left(T_{\text{A2}} \right) - \frac{1}{I_{\text{SN}}}$	$I_{SB}(T_{A1})$)						
EL:	Linearity	fault defin	ed by	<i>е</i> _L =100	$\left \cdot \left \frac{I_{p}}{I_{pN}} - \frac{I_{Sx}}{I_{SN}} \right \right $	-					
	Where I	is any inp			1 111 511	1	n. I _{SN} : see notes	s of F _i (I _o =	0).		
Hrsg.: R&	D-PD NPI	D Bear	rb: DJ		MC-PM	FS				freig.: S	В