SPECIFICATION T60404-N4646-X651 Item no.: 26.01.2022 K-no.: 24507 Date: 25 A Current Sensor modul for 5V-supply voltage For electronic current measurement: DC, AC, pulsed, mixed ..., with a galvanic isolation between primary circuit (high power) and secondary circuit (electronic circuit) Customers Part no .: Page 1 of Customer: Standard type Characteristics Description **Applications** Mainly used for stationary operation in industrial Closed loop (compensation) Excellent accuracy Current Sensor with magnetic applications: Very low offset current field probe AC variable speed drives and servo motor Very low temperature dependency and offset Printed circuit board mounting current drift Static converters for DC motor drives Casing and materials UL-listed Very low hysteresis of offset current short response time Battery supplied applications Switched Mode Power Supplies (SMPS) Wide frequency bandwidth Power Supplies for welding applications Compact design Uninterruptible Power Supplies (UPS) Reduced offset ripple Electrical data - Ratings Primary nominal r.m.s. current 25 IPN V_{out} Output voltage @ IP $2.5 \pm (0.625*I_P/I_{PN})$ Output voltage @ IP=0, TA=25°C 2.5 ± 0.00625 Vout Reference voltage 2.5 ± 0.005 V_{Ref} Turns ratio K_N 1...3:2000 Accuracy - Dynamic performance data min. Unit typ. max. Max. measuring range ±85 I_{P.max} Accuracy @ I_{PN}, T_A= 25°C 0.7 Χ Linearity 0.1 Vout -2,5V Offset voltage @ IP=0, TA= 25°C ±6.25 $\Delta V_{out}/2,5V/\Delta T$ Temperature drift of Vout@ IP=0, TA= -40...85°C 13 26 Response time @ 90% von IPN 300 $\Delta t (I_{P,max})$ Delay time at $di/dt = 100 A/\mu s$ 200 DC...200 Frequency bandwidth **General data** min. Unit typ. max. TA Ambient operating temperature -40 +85 Ts Ambient storage temperature -40 +85 m Mass 12 Supply voltage 4.75 5 5.25 Vc I_{C0} Current consumption 15 Constructed and manufactored and tested in accordance with EN 61800-5-1 (Pin 1 - 6 to Pin 7 - 9) Reinforced insulation. Insulation material group 1. Pollution degree 2

4

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%

mV

ns

ns

kH₂

°C

°C

V

mΑ

ppm/K

		,	0 1 /	0		
Sclear	Clearance (compon	ent without solder pad)		7.5		mm
Screep	Creepage (compon	ent without solder pad)		8.0		mm
V_{sys}	System voltage	overvoltage category 3		RMS	300	V
V_{work}	Working voltage	(tabel 7 acc. to EN61	800-5-1)			
		overvoltage category 2		RMS	650	V
U_{PD}	Rated discharge ve	oltage		peak value	1320	V
Max. potential diffe	erence acc to UL 50	8		RMS	600	V

Date	Name	Issue	Amendment					
26.01.2022	NSch.	83	Applicable do	cuments change on	sheet 3. "The color	of the plastic materia	I added. Minor cha	ange
Hrsg.: KB	3-E		arb: Le		KB-PM IA: KRe			freig.: SB released



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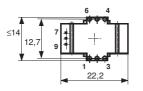
For electronic current measurement: DC, AC, pulsed, mixed ..., with a galvanic isolation between primary circuit (high power) and secondary circuit (electronic circuit) Date: 26.01.2022

Customers Part no.: Page 2 of 4

Mechanical outline (mm):

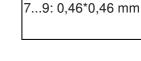
Standard type

General tolerances DIN ISO 2768-c



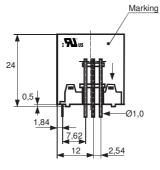
Tolerances grid distance ±0,2mm

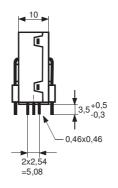
DC = Date Code F = Factory



Marking:

Connections: 1...6: Ø 1 mm

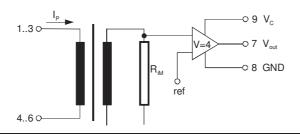




VAT UL-sign 4646-X651 F DC



Schematic diagram



Possibilities of wiring (@ $T_A = 85^{\circ}C$)

primary windings	primary RMS	y current maximal	output voltage effective	turns ratio	primary resistance	wiring
N_P	I _P [A]	Î _{P,max} [A]	$V_{out}(I_P)[V]$	K_N	$R_{P}\left[m\Omega \right]$	
1	25	±85	2.5±0.625	1:2000	0.33	3 1
2	12	±42	2.5±0.600	2:2000	1.5	3 1 4 6>
3	8	±28	2.5±0.600	3:2000	3	3 1

Hrsg.: KB-E	Bearb: Le	KB-PM IA: KRe		freig.: SB
editor	designer	check		released

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Electrical Data

		min.	typ.	max.	Unit
V _{Ctot}	Maximum supply voltage (without function)			7	V
Ic	Supply Current with primary current 1		$A + I_p * K_N + V_0$	out/R _L	mA
l _{out,SC}	Short circuit output current		±20		mA
R_P	Resistance / primary winding @ T _A =25°C		1		$m\Omega$
Rs	Secondary coil resistance @ T _A =85°C			67	Ω
R_{i} ,(V_{out})	Output resistance of Vout			1	Ω
R_L	External recommended resistance of Vout	1			$k\Omega$
CL	External recommended capacitance of Vout			500	pF
$\Delta X_{Ti}/\Delta T$	Temperature drift of X@T _A = -40 +85 °C			40	ppm/K
$\Delta V_0 = \Delta (V_{out} - 2.5V)$	Sum of any offset drift including:		5	10	mV
V_{0t}	Long term drift of V ₀		1		mV
V ₀ T	Temperature drift von V ₀ @ T _A = -40+85°C		4		mV
V_{0H}	Hysteresis of V_{out} @ $I_{P}=0$ (after an overload of 10 x I_{PN}))	2	mV	
$\Delta V_0/\Delta V_C$	Supply voltage rejection ratio			1	mV/V
Voss	Offsetripple (with 1 MHz- filter first order)			60	mV
Voss	Offsetripple (with 100 kHz- filter firdt order)		5	8	mV
Voss	Offsetripple (with 20 kHz- filter first order)		1	2	mV
Ck	Maximum possible coupling capacity (primary – sec Mechanical stress according to M3209/3	condary)	5	10	pF
	Settings: 10 – 2000 Hz, 1 min/Oktave, 2 hours			30g	

Inspection (Measurement after temperature balance of the samples at room temperature)

Vout (IP=IPN	ı) (V)	M3011/6:	Output voltage vs. internal reference (I _P =25A, 40-80Hz)	625±0.7%	mV
V_{out} -2.5 V	$(I_{P}=0)(V)$	M3226:	Offset voltage	± 6.25	mV
Vd	(V)	M3014:	Test voltage, rms, 1 s pin 1 – 6 vs. pin 7 – 9	1,5	kV
Ve	(AQL	1/S4)	Partial discharge voltage acc.M3024 (RMS) with V_{vor} (RMS)	1400 1750	V V

Type Testing (Pin 1 - 6 to Pin 7 - 9)

Designed according standard EN 50178 with insulation material group 1

•	o contract of the contract of	0 1		
V_W	HV transient test according to	M3064 (1,2 μs / 50 μs-wave form)	8	kV
V_d	Testing voltage to M3014	(5 s)	3	kV
Ve	Partial discharge voltage acc	.M3024 (RMS)	1400	V
	with V _{vor} (RMS)		1750	V

Applicable documents

Current direction: A positive output current appears at point I_s , by primary current in direction of the arrow. Enclosures according to IEC529: IP50.

Further standards UL 508, file E317483, category NMTR2 / NMTR8

Temperature of the primary conductor should not exceed 110°C.

"The color of the plastic material is not specified and the current sensor can be supplied in different colors

(e.g. brown, black, white, natural). This has no effect on the specifications or UL approval."

Hrsg.: KB-E Bearb: Le designer	KB-PM IA: KRe		freig.: SB released
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(high power) and secondary circuit

(electronic circuit) Standard type

Customers Part no .: Page 4 of 4

Explanation of several of the terms used in the tablets (in alphabetical order)

Response time (describe the dynamic performance for the specified measurement range), measured as delay time t_r: at $I_P = 0.9$ I_{PN} between a rectangular current and the output voltage V_{OUt} (I_p)

Delay time (describe the dynamic performance for the rapid current pulse rate e.g short circuit current) Δt (I_{Pmax}): measured between I_{Pmax} and the output voltage V_{out}(I_{Pmax}) with a primary current rise of dip/dt ≥ 100 A/µs.

Rated discharge voltage (recurring peak voltage separated by the insulation) proved with a sinusoidal voltage Ve UPD UPD $= \sqrt{2} * V_e / 1.5$

Defined voltage is the RMS valve of a sinusoidal voltage with peak value of 1,875 * UPD required for partial discharge V_{vor} test in IEC 61800-5-1

 V_{vor} $= 1.875 *U_{PD} / \sqrt{2}$

 V_{sys} System voltage RMS value of rated voltage according to IEC 61800-5-1

Working voltage voltage according to IEC 61800-5-1 which occurs by design in a circuit or across insulation V_{work}

Offset voltage between V_{out} and the rated reference voltage of $V_{ref} = 2,5V$. V₀:

 $V_0 = V_{out}(0) - 2,5V$

Zero variation of Vo after overloading with a DC of tenfold the rated value V_{0H}:

Vot: Long term drift of V₀ after 100 temperature cycles in the range -40 bis 85 °C.

X: Permissible measurement error in the final inspection at RT, defined by

$$X = 100 \cdot \left| \frac{V_{out}(I_{PN}) - V_{out}(0)}{0.625V} - 1 \right| \%$$

 $X_{ges}(I_{PN})$: Permissible measurement error including any drifts over the temperature range by the current measurement IPN

$$X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - 2.5V}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \% \ \text{or} \ X_{\rm ges} = 100 \cdot \left| \frac{V_{\rm out} \left(I_{\rm PN} \right) - V_{\it ref}}{0.625 \rm V} - 1 \right| \ \text{or} \ X_{\rm ge$$

 $\text{Linearity fault defined by} \quad \varepsilon_{\text{L}} \! = \! 100 \cdot \left| \frac{\mathbf{I}_{\text{P}}}{\mathbf{I}_{\text{PN}}} - \frac{\mathbf{V}_{\textit{out}}(I_{\textit{P}}) \! - \! V_{\textit{out}}(0)}{\mathbf{V}_{\textit{out}}(I_{\textit{PN}}) \! - \! V_{\textit{out}}(0)} \right| \, \%$ ϵ_{L} :

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editor	designer	check	released	